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PATENT NUMBER and ISSUE DATE

U.S. UTILITY Pat nt Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10081344	02/20/2002	327		2816	ME ->

**APPLICANTS: Mashimo Akira;

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:
JAPAN 2001-044223 02/20/2001
JAPAN 2001-333102 10/30/2001

PG-PUB	DO NOT PUBLISH ☐	RESCIND ☐	
Foreign priority claimed	☐ yes ☐ no		ATTORNEY DOCKET NO
35 USC 119 conditions met	☐ yes ☐ no		
Verified and Acknowledged Examiners's intials			MM4521

TITLE : Signal processing circuit integrating pulse widths of an input pulse signal according to polarities

U.S.DEPT. OF COMM./PAT.& TM-PTO-436L(Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
	Assistant Examiner	Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING	
Amount Due / Date Paid		Sheets Drwg. / Figs.Drwg. / Print Fig.	
☐ TERMINAL DISCLAMER	Primary Examiner	Applicati n Examiner	
	PREPARED FOR ISSUE		

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